

Welcome to DialogClassic Web(tm)

Dialog level 05.05.00D
Last logoff: 23jun05 08:31:29
Logon file405 07jul05 08:39:01

*** ANNOUNCEMENT ***

--UPDATED: Important Notice to Freelance Authors--
See HELP FREELANCE for more information

NEW FILES RELEASED

***CSA Life Sciences Abstracts (File 24)
***Corrosion Abstracts (File 46)
***Materials Business File (File 269)
***Engineered Materials Abstracts (File 293)
***CSA Aerospace & High Technology Database (File 108)
***CSA Technology Research Database (File 23)
***METADEX(r) (File 32)
***FDAnews (File 182)
***German Patents Fulltext (File 324)

RESUMED UPDATING

***Canadian Business and Current Affairs (262)
***CorpTech (559)

Chemical Structure Searching now available in Prous Science Drugs
of the Future (F453), IMS R&D Focus (F445), Beilstein Facts (F390),

Information:

1. Announcements (new files, reloads, etc.)
2. Database, Rates, & Command Descriptions
3. Help in Choosing Databases for Your Topic
4. Customer Services (telephone assistance, training, seminars, etc.)
5. Product Descriptions

Connections:

6. DIALOG(R) Document Delivery
7. Data Star(R)

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/H = Help

/L = Logoff

/NOMENU = Command Mode

Enter an option number to view information or to connect to an online
service. Enter a BEGIN command plus a file number to search a database
(e.g., B1 for ERIC).

?

B 2, 3, 4

07jul05 08:42:32 User222506 Session D33.1
\$0.00 0.321 DialUnits FileHomeBase
\$0.00 Estimated cost FileHomeBase
\$1.06 INTERNET
\$1.06 Estimated cost this search
\$1.06 Estimated total session cost 0.321 DialUnits

SYSTEM:OS - DIALOG OneSearch

File 2:INSPEC 1969-2005/Jun W4

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File 3:INSPEC 1969-1982

10/036, 298

(c) 1993 Institution of Electrical Engineers
 File 4:INSPEC 1983-2005/Jun W4
 (c) 2005 Institution of Electrical Engineers

Set	Items	Description
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?

S ((SEARCH? OR QUER? OR REQUES? OR INQUIR? OR ENQUIR?) (W) ((INTELLECTUAL (W) PROPER
 R MUSIC OR BOOK?))

286916	SEARCH?
71332	QUER?
56538	REQUES?
7950	INQUIR?
3446	ENQUIR?
11658	INTELLECTUAL
187642	PROPERTY
5716	INTELLECTUAL (W) PROPERTY
217734	MEDIA
6540	MOVIE?
652480	SOFTWARE?
192882	VIDEO?
22280	MUSIC
40030	BOOK?

S1 804 ((SEARCH? OR QUER? OR REQUES? OR INQUIR? OR ENQUIR?) (W)
 ((INTELLECTUAL (W) PROPERTY) OR MEDIA OR MOVIE? OR
 SOFTWARE? OR VIDEO? OR MUSIC OR BOOK?))

?

S S1 AND (INTELLECTUAL (W) PROPERTY)/TI

804	S1
1978	INTELLECTUAL/TI
18786	PROPERTY/TI
1128	INTELLECTUAL/TI (W) PROPERTY/TI

S2 0 S1 AND (INTELLECTUAL (W) PROPERTY)/TI

?

S S1 AND (INTELLECTUAL (W) PROPERTY)/CLM

>>>Term "CLM" is not defined in one or more files

804	S1
11658	INTELLECTUAL/CLM
187642	PROPERTY/CLM
5716	INTELLECTUAL/CLM (W) PROPERTY/CLM

S3 0 S1 AND (INTELLECTUAL (W) PROPERTY)/CLM

?

S S1 AND (INTELLECTUAL (W) PROPERTY)/AB

804	S1
10484	INTELLECTUAL/AB
158128	PROPERTY/AB
4866	INTELLECTUAL/AB (W) PROPERTY/AB

S4 0 S1 AND (INTELLECTUAL (W) PROPERTY)/AB

?

S S1 AND (HIERARCH? OR NODE? OR BRANCH? OR LEAF)

804	S1
149142	HIERARCH?
144874	NODE?
153292	BRANCH?
8188	LEAF

```

S5      32  S1 AND (HIERARCH? OR NODE? OR BRANCH? OR LEAF)
?

S (INTELLECTUAL (W) PROPERTY)/TI
      1978  INTELLECTUAL/TI
      18786 PROPERTY/TI
S6      1128 (INTELLECTUAL (W) PROPERTY)/TI
?

S S6 AND (LICENS$ (W) (INTELLECTUAL (W) PROPERT?))
      1128  S6
      0     LICENS$
      11658 INTELLECTUAL
      1788062 PROPERT?
      0     LICENS$(W) INTELLECTUAL(W) PROPERT?
S7      0     S6 AND (LICENS$ (W) (INTELLECTUAL (W) PROPERT?))
?

S S6 AND (SEARCH? OR QUER? OR REQUES? OR INQUIR? OR ENQUIR?)
      1128  S6
      286916 SEARCH?
      71332  QUER?
      56538  REQUES?
      7950   INQUIR?
      3446   ENQUIR?
S8      40   S6 AND (SEARCH? OR QUER? OR REQUES? OR INQUIR? OR
      ENQUIR?)
?

S S8 AND (HIERARCH? OR NODE? OR LEAF? OR TREE?)
      40    S8
      149142 HIERARCH?
      144874 NODE?
      9566   LEAF?
      131792 TREE?
S9      4    S8 AND (HIERARCH? OR NODE? OR LEAF? OR TREE?)
?

T S9/FULL/1-4

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9/9/1 (Item 1 from file: 2)
 DIALOG(R) File 2:INSPEC
 (c) 2005 Institution of Electrical Engineers. All rts. reserv.

7073553 INSPEC Abstract Number: C2001-12-7410D-008
Title: Combining hierarchical filtering, fuzzy logic, and simulation with software agents for IP (intellectual property) selection in electronic design
 Author(s): Jian Liu; Shragowitz, E.; Wei-Tek Tsi
 Author Affiliation: Dept. of Comput. Sci., Minnesota Univ., Minneapolis, MN, USA
 Journal: International Journal on Artificial Intelligence Tools (Architectures, Languages, Algorithms) vol.10, no.3 p.303-23
 Publisher: World Scientific,
 Publication Date: Sept. 2001 Country of Publication: Singapore
 CODEN: IAITEL ISSN: 0218-2130
 SICI: 0218-2130(200109)10:3L:303:CHFF;1-W
 Material Identity Number: P897-2001-003
 Language: English Document Type: Journal Paper (JP)
 Treatment: Practical (P)

Abstract: SAFIPS (Software Agents for IP Selection) environment is designed to improve quality and speed of finding IPs on the Internet for SoC (System-on-Chip) projects. The software agents take responsibility for establishing communication with multiple suppliers of IPs and automatically retrieve information from their databases using metalanguage (XML), and data specifications compliant with ECIX (Electronic Component Information eXchange) specifications. The communication environment is based on dictionaries for terminology and list of parameters, and on registry for the list and Web addresses of suppliers. The information obtained by software agents is analyzed by a system of fuzzy logic rules compiled in the process of a dialog between the customer and SA. The software agent asks the customer questions and provides templates for answers. The answers are automatically converted into the membership functions and fuzzy logic rules that are applied to evaluation of potential candidate IPs. As soon as replies to the query by SA start to come, data are analyzed by software agents and IPs can be excluded from further consideration if there is drastic mismatch between expected values and IP parameters and constraints. The remaining IPs are evaluated by the hierarchical system of rules and ranks are assigned to those which passed the preliminary tests. (30 Refs)

Subfile: C

Descriptors: circuit CAD; digital simulation; fuzzy logic; hypermedia markup languages; industrial property; information retrieval; Internet; software agents

Identifiers: hierarchical filtering; simulation; electronic design; SAFIPS; Software Agents for IP Selection; Internet; system-on-chip projects; databases; metalanguage; ECIX; Electronic Component Information Exchange; dictionaries; fuzzy logic; membership functions; timing diagrams; information retrieval; XML

Class Codes: C7410D (Electronic engineering computing); C4210 (Formal logic); C6170 (Expert systems and other AI software and techniques); C6185 (Simulation techniques); C7210N (Information networks)

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9/9/2 (Item 2 from file: 2)

DIALOG(R)File 2:INSPEC

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6421923 INSPEC Abstract Number: B2000-01-1130B-018, C2000-01-5215-006

Title: Designing with intellectual property

Author(s): Gorla, G.

Author Affiliation: Italtel SpA, Milan, Italy

Conference Title: Proceedings. IEEE Computer Society Workshop on VLSI '99. System Design: Towards System-on-a-Chip Paradigm p.125-32

Editor(s): Smailagic, A.; Brodersen, R.; De Man, H.

Publisher: IEEE Comput. Soc, Los Alamitos, CA, USA

Publication Date: 1999 **Country of Publication:** USA x+133 pp.

ISBN: 0 7695 0152 4 **Material Identity Number:** XX-1999-01042

U.S. Copyright Clearance Center Code: 0 7695 0152 4/99/\$10.00

Conference Title: Proceedings. IEEE Computer Society Workshop on VLSI'99. System Design: Towards System-on-a-Chip Paradigm

Conference Sponsor: IEEE Comput. Soc. Tech. Committee on VLSI

Conference Date: 8-9 April 1999 **Conference Location:** Orlando, FL, USA

Language: English **Document Type:** Conference Paper (PA)

Treatment: Applications (A); Theoretical (T); Experimental (X)

Abstract: A methodology was developed based on IP reuse, aimed at the design of integrated micro-systems. It was tested on a specific custom ASIP (application specific instruction processor) with good performance. IP occurrences are searched and identified inside the system specification code (C has been used for test), before any architectural or partitioning

choice is done. Isolation criteria are their reusability, encapsulation and completeness, while their C++ models are deliberately kept as mutually nestable objects arranged in a number of hierarchical levels. Each such WARELET can be instantiated to full HW instance (like a black box), or full software procedure, or a mix. Every alternative choice gives an IP instance (IPI) whose reuse value is keyed in the IP model and in the parametric synthesis procedures attached to it not in a single specific implementation. The collection of WARELET instances builds up the specific system instance. The design process is a "what-if": inside the code describing a (sub)system some selected warelets are attributed to a HW implementation. HW synthesis generates blocks that communicate within a pre-defined parametric architectural harness either as coprocessors or as execution units of the instruction set. A parallel stepwise co-synthesis is operated for SW code, re-targeting the microprogram control code and the SW algorithm to every new HW configuration. A profiling process gives performance figures to validate or change the choice. These system-level IPs offer innovative opportunities concerning the management of intellectual value within products and the commercial and industrial infrastructure. (9 Refs)

Subfile: B C

Descriptors: circuit CAD; hardware-software codesign; industrial property ; integrated circuit design

Identifiers: intellectual property; IP reuse based methodology; integrated micro-system design; custom ASIP; application specific instruction processor; system specification code; isolation criteria; reusability; encapsulation; completeness; C++ models; WARELET instances; specific system instance; IP model; parametric synthesis procedures; hardware synthesis; parallel stepwise cosynthesis; software code; microprogram control code; profiling process

Class Codes: B1130B (Computer-aided circuit analysis and design); B1265A (Digital circuit design, modelling and testing); B2570A (Semiconductor integrated circuit design, layout, modelling and testing); C5215 (Hardware-software codesign); C6110B (Software engineering techniques); C7410D (Electronic engineering computing)

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9/9/3 (Item 1 from file: 4)

DIALOG(R) File 4:INSPEC

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7073553 INSPEC Abstract Number: C2001-12-7410D-008

Title: Combining hierarchical filtering, fuzzy logic, and simulation with software agents for IP (intellectual property) selection in electronic design

Author(s): Jian Liu; Shragowitz, E.; Wei-Tek Tsi

Author Affiliation: Dept. of Comput. Sci., Minnesota Univ., Minneapolis, MN, USA

Journal: International Journal on Artificial Intelligence Tools (Architectures, Languages, Algorithms) vol.10, no.3 p.303-23

Publisher: World Scientific,

Publication Date: Sept. 2001 Country of Publication: Singapore

CODEN: IAITEI ISSN: 0218-2130

SICI: 0218-2130(200109)10:3L:303:CHFF;1-W

Material Identity Number: P897-2001-003

Language: English Document Type: Journal Paper (JP)

Treatment: Practical (P)

Abstract: SAFIPS (Software Agents for IP Selection) environment is designed to improve quality and speed of finding IPs on the Internet for SoC (System-on-Chip) projects. The software agents take responsibility for establishing communication with multiple suppliers of IPs and automatically

retrieve information from their databases using metalanguage (XML), and data specifications compliant with ECIX (Electronic Component Information eXchange) specifications. The communication environment is based on dictionaries for terminology and list of parameters, and on registry for the list and Web addresses of suppliers. The information obtained by software agents is analyzed by a system of fuzzy logic rules compiled in the process of a dialog between the customer and SA. The software agent asks the customer questions and provides templates for answers. The answers are automatically converted into the membership functions and fuzzy logic rules that are applied to evaluation of potential candidate IPs. As soon as replies to the query by SA start to come, data are analyzed by software agents and IPs can be excluded from further consideration if there is drastic mismatch between expected values and IP parameters and constraints. The remaining IPs are evaluated by the hierarchical system of rules and ranks are assigned to those which passed the preliminary tests. (30 Refs)

Subfile: C

Descriptors: circuit CAD; digital simulation; fuzzy logic; hypermedia markup languages; industrial property; information retrieval; Internet; software agents

Identifiers: hierarchical filtering; simulation; electronic design; SAFIPS; Software Agents for IP Selection; Internet; system-on-chip projects; databases; metalanguage; ECIX; Electronic Component Information Exchange; dictionaries; fuzzy logic; membership functions; timing diagrams; information retrieval; XML

Class Codes: C7410D (Electronic engineering computing); C4210 (Formal logic); C6170 (Expert systems and other AI software and techniques); C6185 (Simulation techniques); C7210N (Information networks)

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9/9/4 (Item 2 from file: 4)

DIALOG(R)File 4:INSPEC

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6421923 INSPEC Abstract Number: B2000-01-1130B-018, C2000-01-5215-006

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Author(s): Gorla, G.

Author Affiliation: Italtel SpA, Milan, Italy

Conference Title: Proceedings. IEEE Computer Society Workshop on VLSI

'99. System Design: Towards System-on-a-Chip Paradigm p.125-32

Editor(s): Smailagic, A.; Brodersen, R.; De Man, H.

Publisher: IEEE Comput. Soc, Los Alamitos, CA, USA

Publication Date: 1999 Country of Publication: USA x+133 pp.

ISBN: 0 7695 0152 4 Material Identity Number: XX-1999-01042

U.S. Copyright Clearance Center Code: 0 7695 0152 4/99/\$10.00

Conference Title: Proceedings. IEEE Computer Society Workshop on VLSI'99.

System Design: Towards System-on-a-Chip Paradigm

Conference Sponsor: IEEE Comput. Soc. Tech. Committee on VLSI

Conference Date: 8-9 April 1999 Conference Location: Orlando, FL, USA

Language: English Document Type: Conference Paper (PA)

Treatment: Applications (A); Theoretical (T); Experimental (X)

Abstract: A methodology was developed based on IP reuse, aimed at the design of integrated micro-systems. It was tested on a specific custom ASIP (application specific instruction processor) with good performance. IP occurrences are searched and identified inside the system specification code (C has been used for test), before any architectural or partitioning choice is done. Isolation criteria are their reusability, encapsulation and completeness, while their C++ models are deliberately kept as mutually nestable objects arranged in a number of hierarchical levels. Each such WARELET can be instantiated to full HW instance (like a black box), or full

software procedure, or a mix. Every alternative choice gives an IP instance (IPI) whose reuse value is keyed in the IP model and in the parametric synthesis procedures attached to it not in a single specific implementation. The collection of WARELET instances builds up the specific system instance. The design process is a "what-if": inside the code describing a (sub)system some selected warelets are attributed to a HW implementation. HW synthesis generates blocks that communicate within a pre-defined parametric architectural harness either as coprocessors or as execution units of the instruction set. A parallel stepwise co-synthesis is operated for SW code, re-targeting the microprogram control code and the SW algorithm to every new HW configuration. A profiling process gives performance figures to validate or change the choice. These system-level IPs offer innovative opportunities concerning the management of intellectual value within products and the commercial and industrial infrastructure. (9 Refs)

Subfile: B C

Descriptors: circuit CAD; hardware-software codesign; industrial property ; integrated circuit design

Identifiers: intellectual property; IP reuse based methodology; integrated micro-system design; custom ASIP; application specific instruction processor; system specification code; isolation criteria; reusability; encapsulation; completeness; C++ models; WARELET instances; specific system instance; IP model; parametric synthesis procedures; hardware synthesis; parallel stepwise cosynthesis; software code; microprogram control code; profiling process

Class Codes: B1130B (Computer-aided circuit analysis and design); B1265A (Digital circuit design, modelling and testing); B2570A (Semiconductor integrated circuit design, layout, modelling and testing); C5215 (Hardware-software codesign); C6110B (Software engineering techniques); C7410D (Electronic engineering computing)

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B 8, 208

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07jul05 08:56:53 User222506 Session D33.2
    $7.14    0.861 DialUnits File2
      $5.80  2 Type(s) in Format  9
    $5.80  2 Types
$12.94 Estimated cost File2
    $1.68    0.202 DialUnits File3
    $1.68 Estimated cost File3
      $5.62    0.677 DialUnits File4
        $5.80  2 Type(s) in Format  9
      $5.80  2 Types
$11.42 Estimated cost File4
      OneSearch, 3 files,  1.740 DialUnits FileOS
    $4.00 INTERNET
    $30.04 Estimated cost this search
    $31.10 Estimated total session cost  2.061 DialUnits
```

SYSTEM:OS - DIALOG OneSearch

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File 8:Ei Compendex(R) 1970-2005/Jun W4
(c) 2005 Elsevier Eng. Info. Inc.
File 208:ONTAP(R) Ei Compendex(R)
(c) 1997 Elsevier Eng. Info. Inc.
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Set Items Description

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S (INTELLECTUAL (W) PROPERTY)/TI

466 INTELLECTUAL/TI
9306 PROPERTY/TI
S1 239 (INTELLECTUAL (W) PROPERTY)/TI
?
S (INTELLECTUAL (W) PROPERTY)/AB
2681 INTELLECTUAL/AB
65634 PROPERTY/AB
S2 1282 (INTELLECTUAL (W) PROPERTY)/AB
?
S S1 AND (SEARCH? OR QUER? OR REQUES? OR INQUIR? OR ENQUIR?)
239 S1
77918 SEARCH?
20638 QUER?
18286 REQUES?
2625 INQUIR?
565 ENQUIR?
S3 11 S1 AND (SEARCH? OR QUER? OR REQUES? OR INQUIR? OR
ENQUIR?)
?
S S2 AND (SEARCH? OR QUER? OR REQUES? OR INQUIR? OR ENQUIR?)
1282 S2
77918 SEARCH?
20638 QUER?
18286 REQUES?
2625 INQUIR?
565 ENQUIR?
S4 58 S2 AND (SEARCH? OR QUER? OR REQUES? OR INQUIR? OR
ENQUIR?)
?
S S3 AND (HIERARCH? OR NODE? OR LEAF? OR TREE?)
11 S3
43763 HIERARCH?
49354 NODE?
9564 LEAF?
48968 TREE?
S5 0 S3 AND (HIERARCH? OR NODE? OR LEAF? OR TREE?)
?
S S4 AND (HIERARCH? OR NODE? OR LEAF? OR TREE?)
58 S4
43763 HIERARCH?
49354 NODE?
9564 LEAF?
48968 TREE?
S6 2 S4 AND (HIERARCH? OR NODE? OR LEAF? OR TREE?)
?

T S6/FULL/1-2

6/9/1 (Item 1 from file: 8)
DIALOG(R) File 8: Ei Compendex(R)
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06709474 E.I. No: EIP04068009286

Title: Time authentication scheme based on PKI framework

Author: Han, Wei; Feng, Fei; Chen, Xiao-Feng; Wang, Yu-Min

Corporate Source: Lab. of Integrated Services Network Xidian Univ., Xi'an 710071, China

Source: Jisuanji Xuebao/Chinese Journal of Computers v 26 n 10 October 2003. p 1355-1360

Publication Year: 2003

CODEN: JIXUDT ISSN: 0254-4164

Language: Chinese

Document Type: JA; (Journal Article) Treatment: A; (Applications)

Journal Announcement: 0402W2

Abstract: In many situations there is a need to certify the time a digital document was created or modified. Time authentication shows its importance in E-commerce and intellectual property protection. At present the main technique widely used is to time-stamp an electronic document. This paper introduces the up-to-date time-stamp protocols, linking protocol, distributed trust protocol and binary tree protocol. Linking Protocol observes the sequence of clients requesting time-stamps and is based on the fact that the hashes they submit cannot be known in advance. Distributed trust protocol selects witnesses randomly in a given group, and constitutes a believable time-stamp via their signature lists. Binary tree protocol combines hashes into one via a binary tree, and publishes the resulting single hash in newspaper advertisement. A time authentication scheme based on the PKI technology is presented. A TSA with its certificate is involved as a trusted third-party witness. Trusted time can be required, and one side or multi-side time authentication request can be responded by TSA. How to extend the lifetime of a time-stamp is also discussed. This paper evaluates the protocols above in terms of the calculation complexity, storage capacity and practicability. Since certificate technique is adopted in digital signature and authentication, the proposed scheme has advantages over other schemes in efficiency and other aspects. 9 Refs.

Descriptors: *Security of data; Public key cryptography; Trees (mathematics); Electronic document exchange; Network protocols; Computer networks

Identifiers: Time authentication; Time stamp; Intellectual property protection; Up-to-date time stamp protocols; Linking protocol; Distributed trust protocol; Binary tree protocol

Classification Codes:

723.2 (Data Processing); 921.4 (Combinatorial Mathematics, Includes Graph Theory, Set Theory); 723.5 (Computer Applications)

723 (Computer Software, Data Handling & Applications); 921 (Applied Mathematics)

72 (COMPUTERS & DATA PROCESSING); 92 (ENGINEERING MATHEMATICS)

6/9/2 (Item 2 from file: 8)

DIALOG(R)File 8:Ei Compendex(R)

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06368381 E.I. No: EIP03187452385

Title: Rapid design space exploration of heterogeneous embedded systems using symbolic search and multi-granular simulation

Author: Mohanty, S.; Prasanna, V.K.; Neema, S.; Davis, J.

Corporate Source: Department of EE-Systems University of Southern California, Los Angeles, CA 90089, United States

Conference Title: Joint Conference on Languages, Compilers and Tools for Embedded Systems and Software and Compilers for Embedded Systems

Conference Location: Berlin, Germany Conference Date: 20020619-20020621

Sponsor: ACM SIGPLAN

E.I. Conference No.: 60896

Source: Joint Conference on Languages, Compilers and Tools for Embedded

Systems and Software and Compilers for Embedded Systems 2002.

Publication Year: 2002

ISBN: 1581135270

Language: English

Document Type: CA; (Conference Article) Treatment: T; (Theoretical)

Journal Announcement: 0305W1

Abstract: In addition to integrating different Intellectual Property cores heterogeneous embedded systems provide several architecture knobs such as voltage, operating frequency, configuration, etc. that can be varied to optimize performance. Such flexibilities results in a large design space making system optimization a very challenging task. Moreover, such systems operate in mobile and other power constrained environments. Therefore, in addition to rapid exploration of a large design space a designer has to optimize both time and energy performance. To address these issues, we propose a hierarchical design space exploration methodology. Our methodology initially uses symbolic constraint satisfaction to rapidly prune the design space. This pruning process is followed by a system wide performance estimation to further reduce the number of candidate designs. Finally, detailed simulation using low-level simulators are performed to select an appropriate design. Our methodology is implemented by integrating two tools, DESERT and HiPerE. into the Model based Integrated simuLation (MILAN)**1 framework. DESERT uses Ordered Binary Decision Diagrams based symbolic search to rapidly explore a large design space and identifies candidate designs that meet the user specified performance constraints. HiPerE provides rapid estimation of system wide energy and latency based on component level simulations and also facilitates energy optimization. MI-LAN provides the required modeling support for these tools and also facilitates component specific multi-granular simulations through seamless integration of various simulators. 25 Refs.

Descriptors: *Embedded systems; Product design; Constraint theory; Performance; Computer simulation

Identifiers: Multi-granular simulation

Classification Codes:

913.1 (Production Engineering); 721.1 (Computer Theory (Includes Formal Logic, Automata Theory, Switching Theory & Programming Theory)); 723.5 (Computer Applications)

722 (Computer Hardware); 913 (Production Planning & Control; Manufacturing); 721 (Computer Circuits & Logic Elements); 723 (Computer Software, Data Handling & Applications)

72 (COMPUTERS & DATA PROCESSING); 91 (ENGINEERING MANAGEMENT)

?

T S3/FULL/1-11

3/9/1 (Item 1 from file: 8)

DIALOG(R) File 8: Ei Compendex(R)

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07440898 E.I. No: EIP05239144693

Title: An introduction to intellectual property

Author: Rowland, William C.

Conference Title: 2004 ASME International Mechanical Engineering Congress and Exposition, IMECE

Conference Location: Anaheim, CA, United States **Conference Date:** 20041113-20041119

Sponsor: ASME, Safety Engineering and Risk Analysis Division; ASME, Technology and Society Division

E.I. Conference No.: 64904

Source: Engineering Technology Management Engineering Technology

management - 2004: Safety Engineering and Risk Analysis, Technology and Society, Engineering Business Management 2004.

Publication Year: 2004

ISBN: 0791847209

Language: English

Document Type: CA; (Conference Article) Treatment: T; (Theoretical)

Journal Announcement: 0506W4

Abstract: The ways of protecting intellectual property using the patent system and other legal mechanisms are discussed. A United States patent provides its owner with the legal right to prevent the unauthorized making, using, selling, and offering for sale in the United States, and the importation into the United States, of the invention set forth and claimed in the patent. A patent is considered a contract between the inventor and the government. In view of the costs in both time and money that may be involved in protecting some innovations, the primary inquiry must probably be is it economically justifiable to protect this idea, not is the idea patentable. (Edited abstract)

Descriptors: *Intellectual property; Public policy; Trademarks; Economics; Product development; Semiconductor devices; Sales; Cost effectiveness; Productivity

Identifiers: Patent system; Legal documents; Inventors; Patent rights

Classification Codes:

902.3 (Legal Aspects); 911.2 (Industrial Economics); 913.1 (Production Engineering); 714.2 (Semiconductor Devices & Integrated Circuits); 911.4 (Marketing)

902 (Engineering Graphics; Engineering Standards; Patents); 901 (Engineering Profession); 911 (Cost & Value Engineering; Industrial Economics); 913 (Production Planning & Control; Manufacturing); 714 (Electronic Components & Tubes)

90 (ENGINEERING, GENERAL); 91 (ENGINEERING MANAGEMENT); 71 (ELECTRONICS & COMMUNICATION ENGINEERING)

3/9/2 (Item 2 from file: 8)

DIALOG(R) File 8: Ei Compendex(R)

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07254006 E.I. No: EIP05068826351

Title: An open source intellectual property optimal selection tool (IPOST - Level 1)

Author: Agarwal, Abhishek; Mallick, Anuj; Ananth, K.S.; Cruz, Joel D.; Alexandridis, Nikitas A.; Ei-Ghazawi, Tarek

Corporate Source: Department of Electrical Engineering George Washington University, Washington, DC 20052, United States

Conference Title: Proceedings of the International Conference on Internet Computing, IC'04

Conference Location: Las Vegas, NV, United States Conference Date: 20040621-20040624

Sponsor: Computer Sci. Research, Education, and Applications Press (CSREA); International Technology Institute (ITI); Korean Society for Internet Information (KSII); World Academy of Science for Information Technology (WAS)

E.I. Conference No.: 64240

Source: Proceedings of the International Conference on Internet Computing, IC'04 Proceedings of the International Conference on Internet Computing, IC'04 v 1 2004.

Publication Year: 2004

Language: English

Document Type: CA; (Conference Article) Treatment: T; (Theoretical); X; (Experimental)

Journal Announcement: 0502W3

Abstract: The emerging issues in embedded system design are IP reuse left bracket 1 right bracket , left bracket 2 right bracket , left bracket 3 right bracket , left bracket 4 right bracket and universal IP search tool. The efficient System-on-Chip (SOC) design depends on a good IP search tool, which can find appropriate IPs from different providers. IPOST is an open source integrated tool, which helps a designer to perform various types of searches and evaluation depending on user provided IP and system level constraints. This paper focuses on the issues of IP selection and matching left bracket 5 right bracket . It also provides a survey of search and retrieval tools, which are currently being used in industry and academia. This paper describes an overview of the complete IPOST and gives a description of level 1 tool. The results not only show the improvement in terms of retrieval time but also show a wider search criterion, which makes the search more effective for the designer. 17 Refs.

Descriptors: *Embedded systems; Systems analysis; Intellectual property; Microprocessor chips; Database systems; Network protocols; Internet; XML; Optimization; Algorithms

Identifiers: System-on-chip (SOC); Exponential rate; Open source integrated tools

Classification Codes:

912.3 (Operations Research); 902.3 (Legal Aspects); 714.2 (Semiconductor Devices & Integrated Circuits); 723.3 (Database Systems); 921.5 (Optimization Techniques)
722 (Computer Hardware); 912 (Industrial Engineering & Management); 902 (Engineering Graphics; Engineering Standards; Patents); 714 (Electronic Components & Tubes); 723 (Computer Software, Data Handling & Applications); 921 (Applied Mathematics)
72 (COMPUTERS & DATA PROCESSING); 91 (ENGINEERING MANAGEMENT); 90 (ENGINEERING, GENERAL); 71 (ELECTRONICS & COMMUNICATION ENGINEERING); 92 (ENGINEERING MATHEMATICS)

3/9/3 (Item 3 from file: 8)

DIALOG(R)File 8: Ei Compendex(R)

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07214208 E.I. No: EIP05028781529

Title: Intellectual property primer: Non-U.S. patent coverage

Author: Bloomer, Scott

Corporate Source: Archer Daniels Midland Co., Decatur, IL, United States

Source: INFORM - International News on Fats, Oils and Related Materials v 15 n 12 December 2004.

Publication Year: 2004

CODEN: IFRMEC **ISSN:** 0897-8026

Language: English

Document Type: JA; (Journal Article) **Treatment:** G; (General Review)

Journal Announcement: 0501W3

Abstract: Companies wanting non-United States patent coverage in more than one country can file their application under the Patent Cooperation Treaty (PCT). Once filed, the application is subjected to an International Novelty Search. Based on this search companies can decide to pursue patenting, or decide that there is too much art in the field to provide a worthwhile patent. (Edited abstract)

Descriptors: *Patents and inventions; Intellectual property; Professional aspects; Public policy; International cooperation; Societies and institutions

Identifiers: Patent application; Patent coverage; Patent Cooperation Treaty (PCT)

Classification Codes:

901.1.1 (Societies & Institutions)
902.3 (Legal Aspects); 901.1 (Engineering Professional Aspects)
902 (Engineering Graphics; Engineering Standards; Patents); 901
(Engineering Profession)
90 (ENGINEERING, GENERAL)

3/9/4 (Item 4 from file: 8)
DIALOG(R)File 8: Ei Compendex(R)
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07046725 E.I. No: EIP04408390379

Title: Intellectual property issues in holography and high tech

Author: Reingand, Nadya

Corporate Source: Celight, Inc., Silver Spring, MD 20904, United States

Conference Title: Practical Holography XVIII: Materials and Applications

Conference Location: San Jose, CA, United States Conference Date:
20040119-20040120

Sponsor: IS and T - Society for Imaging Science and Technology; SPIE -
International Society for Optical Engineering

E.I. Conference No.: 63585

Source: Proceedings of SPIE - The International Society for Optical
Engineering Practical Holography XVIII: Materials and Applications v 5290
2004.

Publication Year: 2004

CODEN: PSISDG ISSN: 0277-786X

Language: English

Document Type: CA; (Conference Article) Treatment: T; (Theoretical)

Journal Announcement: 0410W1

Abstract: The author with technical education background (Ph.D. in
holography) shares her 3+ years of experience working on intellectual
property (IP) issues that includes patents, trademarks, and copyrights. A
special attention is paid to the patent issues: the application procedure,
the patent requirements, the databases for prior art search, how to make
the cost efficient filing. 3 Refs.

Descriptors: *Holography; Intellectual property; Holograms; Database
systems; Education; Security of data; Compact disks; Alcohols; Trademarks;
Smart cards

Identifiers: Wrapping papers; Holographic displays; Credit cards

Classification Codes:

752.3.1 (Sound Reproduction Equipment)

902.3 (Legal Aspects); 723.3 (Database Systems); 901.2 (Education);
723.2 (Data Processing); 722.1 (Data Storage, Equipment & Techniques);
752.3 (Sound Reproduction); 804.1 (Organic Compounds); 722.4 (Digital
Computers & Systems)

743 (Holography); 902 (Engineering Graphics; Engineering Standards;
Patents); 723 (Computer Software, Data Handling & Applications); 901
(Engineering Profession); 722 (Computer Hardware); 752 (Sound Devices,
Equipment & Systems); 804 (Chemical Products Generally)

74 (LIGHT & OPTICAL TECHNOLOGY); 90 (ENGINEERING, GENERAL); 72
(COMPUTERS & DATA PROCESSING); 75 (SOUND & ACOUSTICAL TECHNOLOGY); 80
(CHEMICAL ENGINEERING, GENERAL)

3/9/5 (Item 5 from file: 8)
DIALOG(R)File 8: Ei Compendex(R)
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07018006 E.I. No: EIP04378353434

Title: How to... Understand "intellectual-property" law

Author: Campos, Jeff

Source: Printwear Magazine v 16 n 12 September 2003.

Publication Year: 2003

ISSN: 0898-3313

Language: English

Document Type: JA; (Journal Article) Treatment: G; (General Review)

Journal Announcement: 0409W3

Abstract: Misconceptions regarding the intellectual property law among the printers are discussed. Most popular misconception about copyright law is that copyright domain exists only in public domain. The copyright law states that copyright is granted to the originator of the work from the moment of its creation. It is suggested that apparel decorators must query the customer about their right to use the mark, ask the client for logo's style guide and should not use scanned images in the work. (Edited abstract)

Descriptors: *Laws and legislation; Copyrights; Trademarks; Screen printing; Imaging techniques; Customer satisfaction; Compact disks

Identifiers: Scanned images; Cliparts; Digital files; Logos style guide

Classification Codes:

752.3.1 (Sound Reproduction Equipment); 811.0.3 (Economics, Research & Miscellaneous); 811.0.4 (Engineering & Process Control)

902.3 (Legal Aspects); 745.1 (Printing); 722.1 (Data Storage, Equipment & Techniques); 752.3 (Sound Reproduction); 811.0 (General Topics in Paper Science & Technology); 745.3 (Graphic Arts)

902 (Engineering Graphics; Engineering Standards; Patents); 745 (Printing & Reprography); 741 (Light, Optics & Optical Devices); 912 (Industrial Engineering & Management); 722 (Computer Hardware); 752 (Sound Devices, Equipment & Systems); 811 (Cellulose, Paper & Wood Products)

90 (ENGINEERING, GENERAL); 74 (LIGHT & OPTICAL TECHNOLOGY); 91 (ENGINEERING MANAGEMENT); 72 (COMPUTERS & DATA PROCESSING); 75 (SOUND & ACOUSTICAL TECHNOLOGY); 81 (CHEMICAL ENGINEERING, PROCESS INDUSTRIES)

3/9/6 (Item 6 from file: 8)

DIALOG(R)File 8: Ei Compendex(R)

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06721557 E.I. No: EIP04078021720

Title: Assessing lead-free intellectual property

Author: Casey, Paul; Pecht, Michael

Corporate Source: CALCE Electron. Prod./Systems Center University of Maryland, College Park, MD, United States

Source: Circuit World v 30 n 2 2004. p 46-51+6+8

Publication Year: 2004

CODEN: CIWODV ISSN: 0305-6120

Language: English

Document Type: JA; (Journal Article) Treatment: T; (Theoretical)

Journal Announcement: 0402W4

Abstract: This paper presents the analysis of information collected from numerous patent searches on lead-free alloys. The significance of claim structure and content is discussed in view of the growing number of lead-free patents. Patent analysis software was developed to effectively compare over 350 lead-free alloy patents. A case study was conducted to assess Sn-Ag-Cu and special purpose lead-free candidate alloy intellectual property. The results show that there are a number of patents and patent applications that may affect the use of "popular" Sn-Ag-Cu formulations. 17 Refs.

Descriptors: *Soldering alloys; Lead; Computer software; Brazing; Statistics; Patents and inventions; Laws and legislation; Technology

Identifiers: Lead-free solders; Infrastructure

Classification Codes:

538.1.1 (Soldering)

538.1 (Metal Bonding); 546.1 (Lead & Alloys); 922.2 (Mathematical Statistics); 901.3 (Engineering Research); 902.3 (Legal Aspects)

538 (Welding & Bonding); 546 (Lead, Tin, Zinc, Antimony & Alloys); 723 (Computer Software, Data Handling & Applications); 922 (Statistical Methods); 901 (Engineering Profession); 902 (Engineering Graphics; Engineering Standards; Patents)

53 (METALLURGICAL ENGINEERING, GENERAL); 54 (METALLURGICAL ENGINEERING, METAL GROUPS); 72 (COMPUTERS & DATA PROCESSING); 92 (ENGINEERING MATHEMATICS); 90 (ENGINEERING, GENERAL)

3/9/7 (Item 7 from file: 8)

DIALOG(R)File 8: Ei Compendex(R)

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06595012 E.I. No: EIP03457709561

Title: Re-intermediation in the Republic of Science: Moving from intellectual property to intellectual commons

Author: Uhlig, Paul F.

Corporate Source: Intl. S and T Information Programmes National Academies, Washington, DC 20001, United States

Source: Information Services and Use v 23 n 2-3 2003. p 63-66.

Publication Year: 2003

CODEN: ISUSDX ISSN: 0167-5265

Language: English

Document Type: JA; (Journal Article) Treatment: G; (General Review)

Journal Announcement: 0311W2

Abstract: Public research is largely an open, communitarian, and cooperative system. It is founded on freedom of inquiry, sharing of data, and full disclosure of results by scientists whose motivations are rooted primarily in intellectual curiosity, the desire to influence the thinking of others about the natural world, peer recognition for their achievements, and promotion of the public interest. Although this normative and value structure of public science predated the revolution in digitally networked technologies, it makes it ideally suited to experiment with and exploit those new technological capabilities, which themselves facilitate open, distributed, and cooperative uses of information. It is no coincidence, therefore, that the emergence and early institutionalization of many new paradigms of virtual knowledge-based communities and related information activities have occurred in public science. Examples include open journals, open archives, federated data management networks, community-based open peer review, collaboratories for virtual experiments, and virtual observatories, among others. Taken together, these emerging capabilities represent aspects of a broader trend toward both formal and informal peer production of information in a highly distributed, volunteer, and open environment. Such activities are based on principles that may be more accurately characterized as intellectual commons, rather than intellectual property, and that reflect the communitarian ethos of the republic of science. This presentation will describe several new models of information production, management, and dissemination in public science, and analyze some of the key factors and conditions for their success.

Descriptors: *Intellectual property; Information management; Social aspects; Information use

Identifiers: Intellectual curiosity

Classification Codes:

902.3 (Legal Aspects); 903.2 (Information Dissemination); 901.4 (Impact of Technology on Society); 903.3 (Information Retrieval & Use)

902 (Engineering Graphics; Engineering Standards; Patents); 903
(Information Science); 901 (Engineering Profession)
90 (ENGINEERING, GENERAL)

3/9/8 (Item 8 from file: 8)
DIALOG(R)File 8: Ei Compendex(R)
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06326094 E.I. No: EIP03127401007

Title: Epoline: Communication and transaction for the European intellectual property community
Author: Van Putten, Arnold
Corporate Source: European Patent Office, Rijswijk, Netherlands
Conference Title: Proceedings of the 2000 International Chemical Information Conference
Conference Location: Annecy, France **Conference Date:** 20001022-20001025
E.I. Conference No.: 60469
Source: Proceedings of the 2000 International Chemical Information Conference 2000.
Publication Year: 2000
ISBN: 1873699689
Language: English
Document Type: CA; (Conference Article) **Treatment:** T; (Theoretical)
Journal Announcement: 0303W4

Abstract: The EPO has launched a new strategic initiative, called epoline, which will transform the way the EPO does business. The objective of epoline is to move the EPO away from doing business in paper form towards doing alt patent-related business via the Internet. As part of this, the EPO is setting up the necessary trusted security environment so that customers can be assured of the confidentiality, the authenticity, the integrity, the non-repudiation and the accountability of any transaction. The priorities for the initial developments under epoline are online filing, online file inspection, online register, online search report dispatch and online fee payment. With the launch of epoline Customer Services, the organisation aims to further improve the overall service offered by the EPO and to support the various epoline products and services. This customer contact centre will be a first port of call for all patent-procedure enquiries.

Descriptors: *Online systems; Intellectual property; Information services ; Internet; Online searching

Identifiers: Transactions

Classification Codes:

722.4 (Digital Computers & Systems); 902.3 (Legal Aspects); 903.4 (Information Services); 903.3 (Information Retrieval & Use)
722 (Computer Hardware); 902 (Engineering Graphics; Engineering Standards; Patents); 903 (Information Science); 723 (Computer Software, Data Handling & Applications)
72 (COMPUTERS & DATA PROCESSING); 90 (ENGINEERING, GENERAL)

3/9/9 (Item 9 from file: 8)
DIALOG(R)File 8: Ei Compendex(R)
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06098539 E.I. No: EIP02307030324

Title: Science, intellectual property, and the web
Author: Armbruster, David L.; Hart, Hillary; Pearce, John A.
Corporate Source: Sci. Publications and Lib. Commun. Univ. of Tennessee Health Sci. Ctr., Memphis, TN 38163, United States

Conference Title: STC's 49th Annual Conference
Conference Location: Nashville, TN, United States Conference Date:
20020505-20020508

E.I. Conference No.: 59362

Source: Proceedings/STC, Society for Technical Communication Annual
Conference 2002. p 391-393

Publication Year: 2002

CODEN: PCNCEI

Language: English

Document Type: CA; (Conference Article) Treatment: G; (General Review)

Journal Announcement: 0207W4

Abstract: Many scientific journals have already moved to Web publication, but multiple concerns attend this conversion. This paper considers the technological and other protections available to researchers. It is suggested that scientific journals must also plan carefully for Web publication by allocating funds for protection and proper archiving.

(Edited abstract) 3 Refs.

Descriptors: *Technical writing; Electronic publishing; Natural sciences; World Wide Web; Intellectual property; Security of data; Online searching; Search engines; Information technology

Identifiers: Scientific journals; Scientific publishing; Web publication
Classification Codes:

903.2 (Information Dissemination); 723.5 (Computer Applications); 902.3 (Legal Aspects); 723.2 (Data Processing); 903.3 (Information Retrieval & Use)

903 (Information Science); 723 (Computer Software, Data Handling & Applications); 902 (Engineering Graphics; Engineering Standards; Patents)

90 (ENGINEERING, GENERAL); 72 (COMPUTERS & DATA PROCESSING)

3/9/10 (Item 10 from file: 8)

DIALOG(R) File 8: Ei Compendex(R)

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05750123 E.I. No: EIP01015471087

Title: Integrazione hardware e software: moduli di 'Intellectual Property' per applicazioni Internet

Title: Merging hardware and software: Intellectual Property cores for Internet applications

Author: Bollano, G.; Claretto, S.; Filippi, E.; Torielli, A.; Turolla, M.
Corporate Source: CSELT, Torino, Italy

Source: CSELT Technical Reports v 28 n 4 Aug 2000. p 495-505

Publication Year: 2000

CODEN: CTRPEJ ISSN: 0393-2648

Language: French

Document Type: JA; (Journal Article) Treatment: T; (Theoretical)

Journal Announcement: 0102W3

Abstract: The Internet continuous growth and its widespread applications require different router architectures where flexibility and reconfigurability are the paradigms. In this scenario system engineers and designers ask for adequate and flexible solutions suitable both for prototypes and advanced products. This paper presents hard and soft Intellectual Property (IP) cores, parameterized and programmable for design reuse purposes, that can be efficiently used in address lookup units to forward Internet packet streams. The soft macro, IPCAM, is an innovative Intellectual Property implementing a ternary Content Addressable Memory (CAM), which is capable of solving both exact and longest match operations; it is composed of an hardware module for fast search and a software module for search table configuration. The hard macro is a configurable and physical ternary CAM. Both are suitable for implementing a broad range of

lookup functions. Moreover the efficiency of the proposed cores have been proved on silicon and through hardware emulation platform. (Translated author abstract) 5 Refs.

Descriptors: *Internet; Merging; Computer hardware; Computer software; Data storage equipment; Storage allocation (computer); Computer software reusability; Intellectual property

Identifiers: Content addressable memory (CAM); Intellectual property (IP) cores

Classification Codes:

723.1 (Computer Programming); 723.2 (Data Processing); 722.1 (Data Storage, Equipment & Techniques)

723 (Computer Software); 722 (Computer Hardware)

72 (COMPUTERS & DATA PROCESSING)

3/9/11 (Item 11 from file: 8)

DIALOG(R) File 8: Ei Compendex(R)

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05645638 E.I. No: EIP00095308549

Title: Merging hardware and software: intellectual property cores for internet applications

Author: Bollano, G.; Claretto, S.; Filippi, E.; Torielli, A.; Turolla, M.
Corporate Source: CSELT, Torino, Italy

Conference Title: CICC 2000: 22nd Annual Custom Integrated Circuits Conference

Conference Location: Orlando, FL, USA Conference Date: 19000521-19000524

Sponsor: IEEE Solid State Circuits Society

E.I. Conference No.: 57193

Source: Proceedings of the Custom Integrated Circuits Conference 2000. IEEE, Piscataway, NJ, USA. p 537-540

Publication Year: 2000

CODEN: PCICER ISSN: 0886-5930

Language: English

Document Type: CA; (Conference Article) Treatment: A; (Applications)

Journal Announcement: 0010W2

Abstract: The Internet continuous growth and its widespread applications require different router architectures where flexibility and reconfigurability are the paradigms. In this scenario system engineers and designers ask for adequate and flexible solutions suitable both for prototypes and advanced products. This paper presents hard and soft Intellectual Property cores, parameterized and programmable for design reuse purpose, that can be efficiently used in address lookup units to forward Internet packet streams. The soft macro, IPCAM, is an innovative Intellectual Property implementing a ternary Content Addressable Memory (CAM), which is capable of solving both exact and longest match operations; it is composed of an hardware module for fast search and a software module for search table configuration. The hard macro is a configurable and physical Ternary CAM. Both are suitable for implementing a broad range of lookup functions. Moreover the efficiency of the proposed cores have been proved on silicon and through hardware emulation platform. (Author abstract) 5 Refs.

Descriptors: *Microprocessor chips; Intellectual property; Internet; Integrated circuit layout; Associative storage; Algorithms; Computer simulation

Identifiers: Intellectual property cores; Lookup functions

Classification Codes:

714.2 (Semiconductor Devices & Integrated Circuits); 723.1 (Computer Programming); 722.1 (Data Storage, Equipment & Techniques); 723.5

(Computer Applications)

714 (Electronic Components); 723 (Computer Software); 722 (Computer Hardware)

71 (ELECTRONICS & COMMUNICATIONS); 72 (COMPUTERS & DATA PROCESSING)

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» Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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|--------------------------|---|
| <input type="checkbox"/> | 1. Year of the patent [intellectual property protection, China]
Frank, S.J.; Zhang, Y.P.;
Spectrum, IEEE
Volume 40, Issue 1, Jan. 2003 Page(s):86 - 88
AbstractPlus Full Text: PDF(210 KB) IEEE JNL |
| <input type="checkbox"/> | 2. The effect of industry standard setting on patent licensing and enforcement
Feldman, R.P.; Rees, M.L.; Townshend, B.;
Communications Magazine, IEEE
Volume 38, Issue 7, July 2000 Page(s):112 - 116
AbstractPlus References Full Text: PDF(76 KB) IEEE JNL |
| <input type="checkbox"/> | 3. Intellectual property management practices
Bhaduri, N.; Mathew, M.;
Management of Engineering and Technology, 2003. PICMET '03. Technology Manage
Reshaping the World. Portland International Conference on
20-24 July 2003 Page(s):172 - 177
AbstractPlus Full Text: PDF(481 KB) IEEE CNF |
| <input type="checkbox"/> | 4. IEE Colloquium on 'Management of Intellectual Property' (Digest No.104)
Management of Intellectual Property, IEE Colloquium on
18 Oct 1988
AbstractPlus Full Text: PDF(28 KB) IEE CNF |
| <input type="checkbox"/> | 5. Technology transfer and licensing
Sadler, P.S.;
Management of Intellectual Property, IEE Colloquium on
18 Oct 1988 Page(s):2/1 - 2/4
AbstractPlus Full Text: PDF(4 KB) IEE CNF |
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Irish, V.;
Technology Transfer, IEE Colloquium on
6 Apr 1993 Page(s):4/1 - 4/3
AbstractPlus Full Text: PDF(100 KB) IEE CNF |

10/036,298

<http://ieeexplore.ieee.org/search/searchresult.jsp?query1=intellectual+property&scope1=metad...> 7/7/05

- ☐ **7. Patents: an engineer's guide to protecting intellectual property**
Litwin, L.; Kolodka, J.J.;
Potentials, IEEE
Volume 20, Issue 2, Apr-May 2001 Page(s):10 - 14
[AbstractPlus](#) | Full Text: [PDF\(128 KB\)](#) IEEE JNL

- ☐ **8. Ground rules for software maintenance**
Donner, I.H.;
Computer
Volume 28, Issue 10, Oct. 1995 Page(s):84 - 85
[AbstractPlus](#) | Full Text: [PDF\(156 KB\)](#) IEEE JNL

- ☐ **9. A case of punitive damages (genetic engineering)**
Klee, M.M.;
Engineering in Medicine and Biology Magazine, IEEE
Volume 23, Issue 5, Sept.-Oct. 2004 Page(s):86
[AbstractPlus](#) | Full Text: [PDF\(194 KB\)](#) IEEE JNL

- ☐ **10. Distinguishing between knowledge transfer and technology transfer activities: the organizational factors**
Gopalakrishnan, S.; Santoro, M.D.;
Engineering Management, IEEE Transactions on
Volume 51, Issue 1, Feb. 2004 Page(s):57 - 69
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(368 KB\)](#) IEEE JNL

- ☐ **11. Software patents**
Bragg, A.W.;
IT Professional
Volume 3, Issue 4, July-Aug. 2001 Page(s):39 - 42
[AbstractPlus](#) | Full Text: [PDF\(192 KB\)](#) IEEE JNL

- ☐ **12. Protecting digital content within the home**
Traw, C.B.S.;
Computer
Volume 34, Issue 10, Oct. 2001 Page(s):42 - 47
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(640 KB\)](#) IEEE JNL

- ☐ **13. Chip detectives [reverse engineering]**
Kumagai, J.;
Spectrum, IEEE
Volume 37, Issue 11, Nov. 2000 Page(s):43 - 48
[AbstractPlus](#) | Full Text: [PDF\(4008 KB\)](#) IEEE JNL

- ☐ **14. Beating the system: abuses of the standards adoption process**
Kipnis, J.;
Communications Magazine, IEEE
Volume 38, Issue 7, July 2000 Page(s):102 - 105
[AbstractPlus](#) | Full Text: [PDF\(56 KB\)](#) IEEE JNL

- ☐ **15. Licensing IP embodied in standards, Part 2**
Stern, R.H.;
Micro, IEEE
Volume 19, Issue 5, Sept.-Oct. 1999 Page(s):7 - 9, 81-3
[AbstractPlus](#) | Full Text: [PDF\(400 KB\)](#) IEEE JNL

- ☐ **16. Test in the emerging intellectual property business**
Saxby, R.; Harrod, P.;

Design & Test of Computers, IEEE
Volume 16, Issue 1, Jan.-March 1999 Page(s):16 - 18
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(52 KB\)](#) IEEE JNL

- ☐ **17. Introducing core-based system design**
Gupta, R.K.; Zorian, Y.;
Design & Test of Computers, IEEE
Volume 14, Issue 4, Oct.-Dec. 1997 Page(s):15 - 25
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(228 KB\)](#) IEEE JNL

- ☐ **18. Design of protection and distribution service model for digital broadcasting cont**
Hye Joo Lee; Bum Suk Choi; Jong Won Seok; Jin Woo Hong;
Multimedia and Expo, 2004. ICME '04. 2004 IEEE International Conference on
Volume 1, 27-30 June 2004 Page(s):193 - 196 Vol.1
[AbstractPlus](#) | Full Text: [PDF\(593 KB\)](#) IEEE CNF

- ☐ **19. Building business from technology: the Sandia experience**
Traylor, L.;
System Sciences, 1996., Proceedings of the Twenty-Ninth Hawaii International Confer.
Volume 4, 3-6 Jan. 1996 Page(s):217 - 222 vol.4
[AbstractPlus](#) | Full Text: [PDF\(508 KB\)](#) IEEE CNF

- ☐ **20. Legal and contractual issues in software reuse**
Lim, W.C.;
Software Reuse, 1996., Proceedings Fourth International Conference on
23-26 April 1996 Page(s):156 - 164
[AbstractPlus](#) | Full Text: [PDF\(728 KB\)](#) IEEE CNF

- ☐ **21. Models for university-industry partnership: strategies for competitive performan**
Sheen, P.B.;
Innovation in Technology Management - The Key to Global Leadership. PICMET '97: F
International Conference on Management and Technology
27-31 July 1997 Page(s):81 - 85
[AbstractPlus](#) | Full Text: [PDF\(488 KB\)](#) IEEE CNF

- ☐ **22. Licensing patents and technology by the developer of the technology**
Gilman, M.G.;
Energy Conversion Engineering Conference, 1997. IECEC-97. Proceedings of the 32n
27 July-1 Aug. 1997 Page(s):1280 - 1286 vol.2
[AbstractPlus](#) | Full Text: [PDF\(768 KB\)](#) IEEE CNF

- ☐ **23. Homotopy and intellectual property**
Morishita, S.; Henderson, N.;
Computer Graphics International, 1999. Proceedings
7-11 June 1999 Page(s):156 - 162
[AbstractPlus](#) | Full Text: [PDF\(100 KB\)](#) IEEE CNF

- ☐ **24. Manufacturing fitness for technology transfer**
Varzandeh, J.; Farahbod, K.;
Management of Engineering and Technology, 1999. Technology and Innovation Mana
'99. Portland International Conference on
Volume 1, 25-29 July 1999 Page(s):547 vol.1
[AbstractPlus](#) | Full Text: [PDF\(60 KB\)](#) IEEE CNF

- ☐ **25. IP development and management of IP DB enabling efficient system-on-chip des**
Young-ho Lee; Ki-Won Kwon; Jin-Tea Kim; Chul-Dong Lee;
ASICs, 1999. AP-ASIC '99. The First IEEE Asia Pacific Conference on

23-25 Aug. 1999 Page(s):229 - 232

[AbstractPlus](#) | Full Text: [PDF\(304 KB\)](#) IEEE CNF



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» Key

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IEEE JNL IEEE Journal or Magazine

☐ Check to search only within this results set

IEE JNL IEE Journal or Magazine

Display Format: ☒ Citation ☐ Citation & Abstract

IEEE CNF IEEE Conference Proceeding

Select Article Information

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard



1. **IP repository, a Web based IP reuse infrastructure**
Schindler, P.; Weidenbacher, K.; Zimmermann, T.;
Custom Integrated Circuits, 1999. Proceedings of the IEEE 1999
16-19 May 1999 Page(s):415 - 418

[AbstractPlus](#) | Full Text: [PDF](#)(420 KB) IEEE CNF


2. **Hardware/software co-design for IP objects based on CORBA**
Coors, H.; Madrid, N.M.; Seepold, R.;
Fall VIUF Workshop, 1999.
4-6 Oct. 1999 Page(s):63 - 68

[AbstractPlus](#) | Full Text: [PDF](#)(88 KB) IEEE CNF


3. **Component selection and matching for IP-based design**
Zhang, T.; Benini, L.; De Micheli, G.;
Design, Automation and Test in Europe, 2001. Conference and Exhibition 2001. Proce-
13-16 March 2001 Page(s):40 - 46

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Moraes, F.; Calazans, N.; Marcon, C.; Mesquita, D.; Palma, J.; Blauth, V.;
Communications, IEE Proceedings-
Volume 150, Issue 4, 12 Aug. 2003 Page(s):239 - 243
AbstractPlus Full Text: PDF(643 KB) IEEE JNL |
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Taylor, P.;
Technology of Object-Oriented Languages and Systems, 2000. TOOLS-Pacific 2000. F
International Conference on
20-23 Nov. 2000 Page(s):290 - 301
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| <input type="checkbox"/> | 3. A hierarchy of physical design watermarking schemes for intellectual property p
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Newbould, R.D.; Carothers, J.D.; Rodriguez, J.J.; Holman, W.T.;
Circuits and Systems, 2002. ISCAS 2002. IEEE International Symposium on
Volume 4, 26-29 May 2002 Page(s):IV-862 - IV-865 vol.4
AbstractPlus Full Text: PDF(352 KB) IEEE CNF |
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Peng Wang; Jinsong Liu; Lieguang Zeng;
ASIC, 2003. Proceedings. 5th International Conference on
Volume 1, 21-24 Oct. 2003 Page(s):195 - 198 Vol.1
AbstractPlus Full Text: PDF(370 KB) IEEE CNF |
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Chan, S.S.M.; Qing Li; Pino, J.A.;
Multimedia Software Engineering, 2004. Proceedings. IEEE Sixth International Sympos
13-15 Dec. 2004 Page(s):396 - 402
AbstractPlus Full Text: PDF(168 KB) IEEE CNF |
| <input type="checkbox"/> | 6. Towards a new standard for system-level design
Liao, S.Y.;
Hardware/Software Codesign, 2000. CODES 2000. Proceedings of the Eighth Internati
on
2000 Page(s):2 - 6 |

10/ 036,298

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Yi, Y.; Woods, R.; McCanny, J.V.;
Signals, Systems and Computers, 2003. Conference Record of the Thirty-Seventh Asil
on
Volume 2, 9-12 Nov. 2003 Page(s):1421 - 1425 Vol.2
[AbstractPlus](#) | Full Text: [PDF\(438 KB\)](#) IEEE CNF

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Charbon, E.; Torunoglu, I.;
Computer-Aided Design, 1999. Digest of Technical Papers. 1999 IEEE/ACM Internatio
on
7-11 Nov. 1999 Page(s):591 - 595
[AbstractPlus](#) | Full Text: [PDF\(520 KB\)](#) IEEE CNF

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Rower, T.; Stadler, N.; Thalmann, M.; Kaeslin, H.; Felber, N.; Fichtner, W.;
Custom Integrated Circuits Conference, 2000. CICC. Proceedings of the IEEE 2000
21-24 May 2000 Page(s):533 - 536
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dominated applications**
Vermeulen, F.; Catthoor, F.; Verkest, D.; De Man, H.;
Design, Automation and Test in Europe Conference and Exhibition 2000. Proceedings
27-30 March 2000 Page(s):92 - 98
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Sathe, V.; Ziesler, C.; Papaefihymiou, M.; Kinfl, S.; Kosonocky, S.;
SOC Conference, 2004. Proceedings. IEEE International
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1 [Curriculum 68: Recommendations for academic programs in computer science: a report of tl committee on computer science](#)

William F. Atchison, Samuel D. Conte, John W. Hamblen, Thomas E. Hull, Thomas A. Keenan, William McCluskey, Silvio O. Navarro, Werner C. Rheinboldt, Earl J. Schweppe, William Viavant, David M. You March 1968 **Communications of the ACM**, Volume 11 Issue 3

Full text available: [pdf\(6.63 MB\)](#)

Additional Information: [full citation](#), [references](#), [citing](#)s

Keywords: computer science academic programs, computer science bibliographies, computer sci computer science curriculum, computer science education, computer science graduate programs, undergraduate programs

2 [The "HyTime ": hypermedia/time-based document structuring language](#)

Steven R. Newcomb, Neill A. Kipp, Victoria T. Newcomb
November 1991 **Communications of the ACM**, Volume 34 Issue 11

Full text available: [pdf\(12.96 MB\)](#)

Additional Information: [full citation](#), [references](#), [citing](#)s, [index terms](#)

3 [Special issue on knowledge representation](#)

Ronald J. Brachman, Brian C. Smith
February 1980 **ACM SIGART Bulletin**, Issue 70

Full text available: [pdf\(13.13 MB\)](#)


Additional Information: [full citation](#), [abstract](#)

In the fall of 1978 we decided to produce a special issue of the SIGART Newsletter devoted to a s knowledge representation research. We felt that there were two useful functions such an issue co hoped to elicit a clear picture of how people working in this subdiscipline understand knowledge r research, to illuminate the issues on which current research is focused, and to catalogue what app techniques are currently being developed. Secon ...

4 [Experiments in social data mining: The TopicShop system](#)

Brian Amento, Loren Terveen, Will Hill, Deborah Hix, Robert Schulman
March 2003 **ACM Transactions on Computer-Human Interaction (TOCHI)**, Volume 10 Issue 1

10/036, 298

Full text available:  [pdf\(377.92 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index](#)


Social data mining systems enable people to share opinions and benefit from each other's experience mining and redistributing information from computational records of social activity such as Usenet usage history, citations, or hyperlinks. Some general questions for evaluating such systems are: (1) is information valuable? and (2) do interfaces based on the information improve user task performance on *TopicShop*, a system ...

Keywords: Cocitation analysis, collaborative filtering, computer-supported cooperative work, information social filtering, social network analysis

5 Visual mapping of articulable tacit knowledge

Peter Anthony Busch, Debbie Richards, C. N. G. 'Kit' Dampney

December 2001 **Australian symposium on Information visualisation - Volume 9**

Full text available:  [pdf\(1.34 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index](#)

Tacit knowledge has long been recognised, however its research has focused largely on who is the store of knowledge, rather than taking this one step further and elaborating on just how well this is distributed throughout the organisational domain. We focus our efforts on the IS organisational domain, by working with computing workplace professionals as opposed to the entire intra-organisational workplace. Our research follows the Sternberg example, however ...

Keywords: AI0102 case study, AI0106 exploratory study, AI0801 positivist perspective, AI0802 perspective, AL01 knowledge representation, AL04 knowledge acquisition, DD07 information flow: articulable tacit knowledge, codified knowledge, diffusion of knowledge, social network analysis, tacit knowledge

6 Intellectual property rights for digital library and hypertext publishing systems: an analysis of

Pamela Samuelson, Robert J. Glushko

September 1991 **Proceedings of the third annual ACM conference on Hypertext**

Full text available:  [pdf\(992.80 KB\)](#)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

7 Applications: Building a massively multiplayer game for the million: Disney's Toontown Online

Mark R. Mine, Joe Shochet, Roger Hughston

October 2003 **Computers in Entertainment (CIE)**, Volume 1 Issue 1

Full text available:  [pdf\(2.37 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [index terms](#)



This paper presents an overview of the lessons learned building Disney's Toontown Online, a 3D online game (MMP) for children ages seven and older. The paper is divided into three main parts. The first part is a design highlights of Toontown Online and focuses on the challenge of building an MMP for kids. In the second part, we discuss ways of incorporating kid-friendly socialization into an MMP. The second part of the paper presents Panda-3D, the VR Studio's open ...

Keywords: 3D, Internet, computer graphics, multiplayer games, online games

8 A digital on-demand video service supporting content-based queries

T. D. C. Little, G. Ahanger, R. J. Folz, J. F. Gibbon, F. W. Reeve, D. H. Schelleng, D. Venkatesh

September 1993 **Proceedings of the first ACM international conference on Multimedia**

Full text available:  [pdf\(177.72 KB\)](#)  [ps\(4.19 MB\)](#)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

Keywords: applications, content-based retrieval, multimedia databases, temporal data managen

9 We Talk to Everybody

Marjorie Richardson, Jason Schumaker, David Penn

June 2000 **Linux Journal**

Full text available:  [html\(96.53 KB\)](#) Additional Information: [full citation](#), [abstract](#), [index terms](#)

A quick look at some of the people who helped make Linux possible.

10 Emerging applications: DRM: doesn't really mean digital copyright management

L. Jean Camp

November 2002 **Proceedings of the 9th ACM conference on Computer and communications se**

Full text available:  [pdf\(258.91 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index](#)


Copyright is a legal system embedded in a larger technological system. In order to examine the fi is critical to examine the larger technological context of copyright: analog media and printed paper copyright system includes both the explicit mechanisms implemented by law and the implicit mec the technologically determinant features of paper and print. In order to prevent confusion betwee and economic elements ...

Keywords: DRM, DeCSS, copyright, design for values, ethics, fair use, intellectual property, sciel studies

11 Constructing, organizing, and visualizing collections of topically related Web resources

Loren Terveen, Will Hill, Brian Amento

March 1999 **ACM Transactions on Computer-Human Interaction (TOCHI)**, Volume 6 Issue 1

Full text available:  [pdf\(303.62 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index](#)


For many purposes, the Web page is too small a unit of interaction and analysis. Web sites are sti documents consisting of many pages, and users often are interested in obtaining and evaluating e topically related sites. Once such a collection is obtained, users face the challenge of exploring, cc organizing the items. We report four innovations that address these user needs: (1) we replaced Web site

Keywords: cocitation analysis, collaborative filtering, computer supported cooperative work, info social filtering, social network analysis

12 Spoken dialogue technology: enabling the conversational user interface

Michael F. McTear

March 2002 **ACM Computing Surveys (CSUR)**, Volume 34 Issue 1

Full text available:  [pdf\(987.69 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index](#)

Spoken dialogue systems allow users to interact with computer-based applications such as datab systems by using natural spoken language. The origins of spoken dialogue systems can be traced Intelligence research in the 1950s concerned with developing conversational interfaces. However, last decade or so, with major advances in speech technology, that large-scale working systems h and, in some cases, introduced into commerc ...

Keywords: Dialogue management, human computer interaction, language generation, language recognition, speech synthesis

13 DRM experience: Analysis of security vulnerabilities in the movie production and distribution

Simon Byers, Lorrie Cranor, Dave Korman, Patrick McDaniel, Eric Cronin

October 2003 **Proceedings of the 2003 ACM workshop on Digital rights management**

Full text available:  pdf(285.80 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index](#)


Unauthorized copying of movies is a major concern for the motion picture industry. While unauthorized movies have been distributed via portable physical media for some time, low-cost, high-bandwidth Internet peer-to-peer file sharing networks provide highly efficient distribution media. Many movies are shared on these networks shortly after, and in some cases prior to, theatrical release. It has been argued that unauthorized copies directly...

Keywords: digital rights management, file sharing, insider attacks, multimedia, physical security

14 A logic base tool set for real-time Ada software development

Michael Moore

June 1991 **Proceedings of the eighth annual Washington Ada symposium & summer SIGAda software: foundation for competitiveness**

Full text available:  pdf(1.50 MB)


Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This is a report on work conducted privately that explores the use of predicate logic to reason about safety and reliability issues associated with embedded real-time systems. Embedded real-time systems make greater demands on engineers than non-real-time systems whose functional complexity is of similar degree. The effort attempts to provide methods and tools for dealing with both the functional and temporal aspects of engineering. The goal is to improve the effectiveness of...

15 Rethinking the design of the Internet: the end-to-end arguments vs. the brave new world

Marjory S. Blumenthal, David D. Clark

August 2001 **ACM Transactions on Internet Technology (TOIT)**, Volume 1 Issue 1

Full text available:  pdf(176.33 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index](#)

This article looks at the Internet and the changing set of requirements for the Internet as it becomes more oriented toward the consumer, and used for a wider set of purposes. We discuss a set of principles that guided the design of the Internet, called the end-to-end arguments, and we conclude that there is a set of new requirements now emerging that could have the consequence of compromising the Internet's core principles. Were...

Keywords: ISP, Internet, end-to-end argument

16 Survey articles: Data mining for hypertext: a tutorial survey

Soumen Chakrabarti

January 2000 **ACM SIGKDD Explorations Newsletter**, Volume 1 Issue 2

Full text available:  pdf(1.19 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

With over 800 million pages covering most areas of human endeavor, the World-wide Web is a fertile mining ground to make a difference to the effectiveness of information search. Today, Web surfing is done through two dominant interfaces: clicking on hyperlinks and searching via keyword queries. This is a tentative and unsatisfactory. Better support is needed for expressing one's information need and for presenting the result in more structured ways than available...

17 Copyrights and access-rights: How DRM-based content delivery systems disrupt expectations

Deirdre K. Mulligan, John Han, Aaron J. Burstein

October 2003 **Proceedings of the 2003 ACM workshop on Digital rights management**

Full text available:  pdf(416.68 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#), [index](#)

We set out to examine whether current, DRM-based online offerings of music and movies accord with current expectations regarding the personal use of copyrighted works by studying the behavior of film online distribution services. We find that, for the most part, the services examined do not accord with expectations of personal use. The DRM-based services studied restrict personal use in a manner inconsistent with expectations governing the purchase and ...

Keywords: access control, content distribution, copyright, digital rights management, fair use, p

18 Image Retrieval from the World Wide Web: Issues, Techniques, and Systems

M. L. Kherfi, D. Ziou, A. Bernardi

March 2004 **ACM Computing Surveys (CSUR)**, Volume 36 Issue 1

Full text available:  pdf(294.13 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

With the explosive growth of the World Wide Web, the public is gaining access to massive amount However, locating needed and relevant information remains a difficult task, whether the informati Text search engines have existed for some years now and have achieved a certain degree of succ the large number of images available on the Web, image search engines are still rare. In this artic order to allow people to profi ...

Keywords: Image-retrieval, World Wide Web, crawling, feature extraction and selection, indexin search, similarity

19 Session H: Multimedia: A 2-D MPEG-4 multimedia authoring tool

D. W. Viljoen, A. P. Calitz, N. L. O. Cowley

February 2003 **Proceedings of the 2nd international conference on Computer graphics, virtua visualisation and interaction in Africa**

Full text available:  pdf(925.68 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

MPEG-4 is an ISO/IEC standard developed by MPEG and promises to be a revolutionary force in ti MPEG-4 allows multimedia authors to create interactive multimedia content that can be streamed over high and low bandwidth connections. The standard has the potential to bring interactive mul audience, for instance providing interactive television or streaming video to a cell phone. Any mul only be successful if it is a ...

Keywords: MPEG-4 framework, MPEG-4 multimedia authoring tool

20 Hypertext'91 trip report

Lynda Hardman

July 1992 **ACM SIGCHI Bulletin**, Volume 24 Issue 3

Full text available:  pdf(976.62 KB)

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This was the third US-based hypertext conference (following on from the '87 and '89 conferences) 450 from the USA and 125 from the rest of the world.

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<input type="checkbox"/>	L16 5846131 6034930 6102800 6161121 6359985 6359998 6434535 6470446 6549894 6636867 5404291 6226618 6226618 6141754 5524898 5293614 4509922 4934711 5803461 6202056 6343738 5313581 5644498 6615191 5758348 6345378)	604
<input type="checkbox"/>	L15 ((L13 or L14) and ((intellectual near property) same (hierarch\$ or tree\$ or level\$ or node\$ or branch\$))	9
<input type="checkbox"/>	L14 (intellectual near property).ab.	40
<input type="checkbox"/>	L13 (intellectual near property).ti.	17
<input type="checkbox"/>	L12 ((intellectual near property near right\$) same (hierarch\$ or tree\$ or level\$ or node\$ or branch\$))	9
<input type="checkbox"/>	L11 ((intellectual near property near right\$) near (hierarch\$ or tree\$ or level\$ or node\$ or branch\$))	0
<input type="checkbox"/>	L10 (intellectual near property near right\$).ab.	2
<input type="checkbox"/>	L9 (intellectual near property near right\$).ti.	0

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<input type="checkbox"/>	L5	L1 and (movie or movies or song or songs or program or programs or software or video or games or book or text or books or videos or music)	1
<input type="checkbox"/>	L4	L3 and media	1
<input type="checkbox"/>	L3	L2 and (intellectual near property)	1
<input type="checkbox"/>	L2	L1 and (right or rights)	1
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END OF SEARCH HISTORY